



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerraguttugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.
काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६, ०१५ तेलंगणा राज्य, भारत
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015, తెలంగాణ రాష్ట్రం, భారతదేశము
Office of the Controller of Examinations

for Supplementary Students

2018 to 2021 Admitted Batches

No. ESE Schedule Supplementary/ B.Tech. III-Sem. (URR18)/2025 /652

Date: 26-04-2025

End Semester Examinations (ESE) For B.Tech. III-Semester (URR18) Supplementary Examinations, April/May-2025

TIME - TABLE

Examination Timings: FN: 10.00 am to 01.00 pm

Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSE(N)	ECI	CSE (IoT)	CSE (AIML)
22.05.2025 Thursday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
24.05.2025 Saturday	OE-I Strength of Materials	Mechanics of Solids	Object Oriented Programming through JAVA	Electrical Measurements & Instrumentation	OE-I Object Oriented Programming	Operating Systems	Object Oriented Programming through JAVA	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
27.05.2025 Tuesday	Fluid Mechanics	Material Science and Metallurgy	Discrete Mathematics	Electromagnetic Fields	Signals & Systems	Discrete Mathematics	Database Management Systems Operating Systems	Electronic Measurements and Sensors	Fundamentals of Internet of Things	Operating Systems
29.05.2025 Thursday	Surveying	Engineering Thermodynamics	Computer Architecture and Organization	Network Theory	Network Analysis	Computer Architecture and Organization	Computer Architecture and Organization	Signals Systems and Random Processes	Computer Networks	Computer Organization and Architecture
31.05.2025 Saturday	Construction Materials	Machine Drawing	Advanced Data Structures	Electronic Devices and Circuits	Analog Circuits - I	Advanced Data Structures Object Oriented Programming through C++	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
03.06.2025 Tuesday	--	--	Digital Electronics	--	Switching Theory & Logic Design	Switching Theory and Logic Design	Digital Electronics	Digital Circuits and Logic Design	Digital Electronics	Formal Languages and Automata Theory
05.06.2025 Thursday	--	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	--	--	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	--	Essence of Indian Traditional Knowledge	--

Candidate should occupy his/her seat at least **10 minutes before** the commencement of the examination.

To: Exam Branch Notice Boards

Copy to: 1. Principal for favour of information 2. Dean, Academic Affairs

3. HOD's - with a request to display in the Dept Notice Boards & share in the Faculty & Class WhatsApp groups

4. Training & Placement Officer 5. I/c Web Team - with a request to display on the www.kitsw.ac.in/examinations/Examinations_schedules.html

S. Rajeswari
Controller of Examinations
5/4
26/4